



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

5A1765

10-1-02

Applicant: Randall Scott Parker

Serial No.: 10/034,483

Filed : December 28, 2001

For : METHOD FOR INTERCONNECTING
MAGNETORESISTIVE MEMORY BITS

Group Art Unit: 1765

Docket No.: 13312-106

Commissioner for Patents
Washington, D.C. 20231

I CERTIFY THAT THIS PAPER IS BEING DEPOSITED WITH
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COMMISSIONER OF PATENTS AND TRADEMARKS,
WASHINGTON, D.C. 20231, ON AUGUST 16, 2002 (37 CFR
1.8a).

Alan D. Kamrath

PRELIMINARY AMENDMENT

Dear Sir:

Please amend the above application as follows:

IN THE CLAIMS

Please replace pages 15 - 34 with the clean copies of pages 15 - 24 attached.

REMARKS

The word "step" has been removed from the claims to avoid the automatic application of
35 U.S.C. § 112.

Please consider the enclosed prior art listed on the further enclosed FORM PTO-1449.

Favorable consideration and allowance of the present application are respectively
requested.

Respectfully submitted,

Randall Scott Parker

Dated: August 16, 2002

By:

Alan D. Kamrath

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